Attorney's Docket No. 042390.P15884

JUL 2 4 2006 JUL 2 TRADEMARK OFFICE

Examiner:

Art Unit:

PATENT

Ray, Gopal C.

2111

IN THE UNITED STATES PATENT

In Re Application of:

Stalinselvaraj Jeyasingh, et al.

Application No.: 10/676,890

Filed:

September 30, 2003

For: CHIPSET SUPPORT FOR

MANAGEMENT HARDWARE INTERRUPTS IN A VIRTUAL

MACHINE SYSTEM

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: July 20, 2006

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INFORMATION DISCLOSURE			Application Number	10/676,890			
			Filing Date	September 30, 2003			
STATEMENT BY APPLICANT (use as many sheets as necessary)			First Named Inventor:	Stalinselvaraj Jeyasingh			
			Art Unit	2111			
				JUL 2 4 2006	Examiner Name	Ray, G	opal C.
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			U.S	S. PATENT DOCUM	ENTS		
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		Num	ber-Kind Code ² (If known)				Figures Appear
		US-	4,837,674	06-06-1989	Taken, Hisao		
		US-	4,860,190	08-22-1989	Kaneda; Saburo, et al		
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-		IRASCHKO, R.R., et al., "A highly efficient path-resolution protocol for management of optical network transport integrity," (abstract only), IEEE Explore Vol. 19, Issue 5, Publication Date: May 2000, 5 pages.		

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